

TELANGANA UNIVERSITY
S.S.R. DEGREE COLLEGE, NIZAMABAD (C.C:5029)
VI SEMESTER INTERNAL ASSESSMENT I EXAMINATIONS
ELECTRONICS (8051 Micro Controller & its application)

I. Multiple choice questions.

- 1) Size of the Internal RAM? ()
a) 128 b) 64 c) 256 d) 512
- 2) Registers used in MUL & DIV ()
a) A, B b) R0, R1 c) R2, R3 d) R4, R5
- 3) Register used in pointing Register? ()
a) R0, R1 b) R2, R3 c) R4, R5 d) R6, R7
- 4) Which register holds the serial data interrupt bits TI & RI ()
a) SCON b) PCON c) TCON d) SMOD
- 5) Address of the stack pointer register (SP) when 8051 is reset ()
a) 00 b) 07 c) 0F d) 08
- 6) Which port has no alternate function? ()
a) Port '0' b) Port '1' c) Port '2' d) Port '3'
- 7) Address of a subroutine that handles timer '1' interrupt? ()
a) 0003H b) 000BH c) 0013H d) 001BH
- 8) Inter ROM size in 8031? ()
a) 0 bytes b) 4K bytes c) 8K bytes d) 64K bytes
- 9) No. of Register banks in 8051 μ c are? ()
a) 10 b) 04 c) 14 d) 24
- 10) Ports used for external memory access? ()
a) Ports '1' & '3' b) Ports '0' & '4' c) Ports '0' & '2' d) Ports '1' & '2'

II. Multiple choice questions.

- 1) The pc of 8051 is _____ bits wide.
- 2) Registers 'A' & 'B' are _____ bits wide each
- 3) _____ is size of SP register.
- 4) _____ ports of 8051 μ c need pull up register to function as I/o port.

- 5) The crystal for the oscillator of the chip is to be connected to pins _____ & _____
- 6) Upon RESET, all the bits of ports are configured as _____
- 7) _____ ports of the 8051 μ c are bit addressable.
- 8) In accessing stack, we must esse _____ addressing mode.
- 9) _____ address is assigned to register R2 of Bank2.
- 10) The instruction "MOV A, 404" uses _____ addressing mode.

III. Answer short questions.

5x1=5

- 1) Why do we need sub ran times?
- 2) In what way is the LCALL instruction different them ACALL?
- 3) Show simple statements to send 99H to ports P1 & P2?
- 4) SFR means? Give the address of 'A' & 'B'
- 5) Explain the function of SWAP?