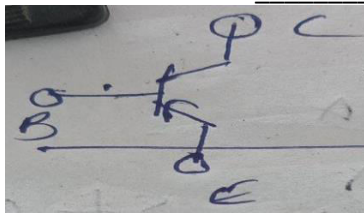


**TELANGANA UNIVERSITY**  
**S.S.R. DEGREE COLLEGE, NIZAMABAD (C.C:5029)**  
**V SEMESTER INTERNAL ASSESSMENT II EXAMINATIONS**  
**BASIC ELECTRONICS QUESTION BANK**

1. The energy gap of pure semiconductor is [a]  
 a. 1 ev                                      b. 6 ev                                      c. 0 ev                                      d. 3 ev
2. The majority carriers in N type semi conductor [b]  
 a. Holes                                      b. Electrons                                      c. Both a & b                                      d. None
3. The potential barrier voltage in Ge diode [a]  
 a. 0.3 V                                      b. 0.5 V                                      c. 0.6 V                                      d. 0.1 V
4. The capacitance of the diode represents [a]  
 a. Varicap diode                                      b. Zener diode                                      c. Tunnel diode                                      d. PN junction diode
5. The PNP transistor consists of \_\_\_\_\_ majority carries. [b]  
 a. Electrons                                      b. Holes                                      c. Donors                                      d. Acceptors
6. The NPN transistor consists of \_\_\_\_\_ minority carries [b]  
 a. Electrons                                      b. holes                                      c. donors                                      d. Acceptors
7. The arrow mark on the transistor symbol indicates [a]  
 a. Positive current                                      b. Negative current                                      c. Minority current                                      d. Majority current
8. The early effect happens in? [c]  
 a. CE                                      b. CC                                      c. CB                                      d. FET
9. The Ohm's law obeys in? [c]  
 a. Inductor                                      b. capacitor                                      c. Resistor                                      d. transistor
10. The peak inverse voltage (PIV) in FWR is? [b]  
 a.  $V_0$                                       b.  $2V_0$                                       c.  $\frac{V_0}{2}$                                       d.  $\frac{V_0}{\sqrt{2}}$

11. The HWR maximum efficiency is 40.6%
12. The FWR ripple factor is 0.48

13. The symbol of PN junction is \_\_\_\_\_



14. The symbol PNP transistor is \_\_\_\_\_
15. The cut-in-voltage of Si diode is 0.5 to 0.7 V
16. The depletion region consist of donor's and Acceptor ions
17. The type of transformer used in FWR is central tapped
18. The Tunnel diode used in forward bias
19. The regultant vesistance of 'n' series resistance is  $R = R_1 + R_2 + R_3 + \dots + R_n$
20. The relation between coefficient of coupling (k), LAM is  $K = \frac{M}{\sqrt{L_1 L_2}}$

Short Answers.

21. State KVL?
22. State KCL?
23. Define depletion region?
24. What is rectifier?
25. What is meant by biasing?